

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10512017			
<b>Filing Date:</b>	29-Apr-2005			
<b>Title of Invention:</b>	SEMICONDUCTOR COMPONENT WITH INTEGRATED CAPACITANCE STRUCTURE AND METHOD FOR FABRICATION THEREOF			
<b>First Named Inventor/Applicant Name:</b>	Thomas Benetik			
<b>Filer:</b>	Edward Machado/Nancy Tarwater			
<b>Attorney Docket Number:</b>	10808/169			
Filed as Large Entity				
<b>U.S. National Stage under 35 USC 371 Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
Utility Appl issue fee	1501	1	1510	1510
Publ. Fee- early, voluntary, or normal	1504	1	300	300

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
Total in USD (\$)				1810